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A CMOS CELL FOR PARALLELLY LOADABLE COUNTERS

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**Abstract**

A CMOS flip-flop cell which can be used in counters is designed. SPICE model, the transient response and the CMOS layout are presented. When used in counters, the flip-flop can be loaded to initialize the count.

**Keywords:**

CMOS, counters, computer aided design, SPICE, complementary metal oxide semiconductors