

Comment On Floating Inductance Simulation Based On Currentconveyors

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Summary

For the original article see *ibid.*, vol. 33, p. 1748-49 (1997). This comment relates to the aforementioned Letter which presents a floating inductance simulation circuit. The proposed circuit uses four plus-type second-generation current-conveyors (CCII+), two floating resistors and one grounded capacitor. It is shown here that such a realisation is a special case of three already published circuits for floating inductance realisation

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