

**Sabri A. Mahmoud,**

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**Key words:** Hardware programming languages, AHPL compiler, digital system design,  
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## CIRCUIT PLOTTING OF THE AHPL COMPILER OUTPUT

S. A. Mahmoud

### ABSTRACT

Hardware Programming Languages (HPL's) are important for computer digital system specification, design, and implementation. A hardware programming language (AHPL) and several other languages have been proposed. The output of the AHPL compiler are tables of connection lists defining the elements used and how they are inter-related. The information about the digital system is included in those tables, however, they are in a form that is not easy to visualize.

This paper addresses the transformations of the output of the AHPL compiler into a digital circuit graph. This output clearly indicates the type and number of elements used, allows the change of gate types, element sizes are modifiable according to the number of input lines, and enables an efficient utilization of space and number of elements used.

**Key words :** Hardware programming languages, AHPL compiler, digital system design, circuit plotting.